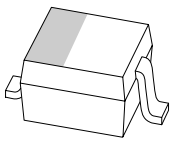
查询PESD1LIN供应商



PESD1LIN

LIN bus ESD protection diode in SOD323

Rev. 01 — 26 October 2004 Product data sheet

1. Product profile

1.1 General description

PESD1LIN in very small SOD323 (SC-76) SMD plastic package designed to protect one automotive LIN bus line from the damage caused by ElectroStatic Discharge (ESD) and other transients.

1.2 Features

- ESD protection of one automotive LIN bus line
- Asymmetrical diode configuration ensures an optimized Electromagnetical Immunity of a LIN Electronic Control Unit (ECU)
- Due to the integrated diode structure only one very small SOD323 package is needed
- Max. peak pulse power: P_{PP} = 160 W at t_p = 8/20 µs
- Low clamping voltage: $V_{(CL)R}$ = 40 V at I_{PP} = 1 A
- Ultra low leakage current: I_{RM} < 1 nA
- ESD protection of up to 23 kV
- IEC 61000-4-2, level 4 (ESD)
- IEC 61000-4-5 (surge); I_{PP} = 3 A at t_p = 8/20 µs

1.3 Applications

- LIN bus protection
- Automotive applications

1.4 Quick reference data

Table 1: Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{RWM}	reverse stand-off voltage					
	PESD1LIN (15 V)		-	-	15	V
	PESD1LIN (24 V)		-	-	24	V
C_d	diode capacitance	V_R = 0 V; f = 1 MHz	-	13	17	pF

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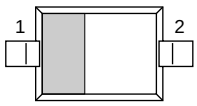
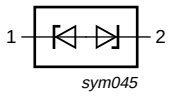
2. Pinning information

Table 2: Pinning

Pin	Description	Simplified outline	Symbol
1	cathode 1 (15 V)		
2	cathode 2 (24 V)		

3. Ordering information

Table 3: Ordering information

Type number	Package		
	Name	Description	Version
PESD1LIN	SC-76	plastic surface mounted package; 2 leads	SOD323

4. Marking

Table 4: Marking codes

Type number	Marking code
PESD1LIN	AM

5. Limiting values

Table 5: Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
P_{PP}	peak pulse power	8/20 µs	[1]	-	160	W
I_{PP}	peak pulse current	8/20 µs	[1]	-	3	A
T_j	junction temperature			-	150	°C
T_{amb}	ambient temperature			–65	+150	°C
T_{stg}	storage temperature			–65	+150	°C

[1] Non-repetitive current pulse 8/20 µs exponentially decaying waveform; see Figure 1.

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Table 6: ESD maximum ratings

Symbol	Parameter	Conditions		Min	Max	Unit
ESD	electrostatic discharge capability	IEC 61000-4-2 (contact discharge)	[1]	-	23	kV
		HBM MIL-STD883		-	10	kV

[1] Device stressed with ten non-repetitive ElectroStatic Discharge (ESD) pulses; see Figure 2.

Table 7: ESD standards compliance

ESD Standard	Conditions
IEC 61000-4-2, level 4 (ESD); see Figure 2	> 15 kV (air); > 8 kV (contact)
HBM MIL-STD883, class 3	> 4 kV

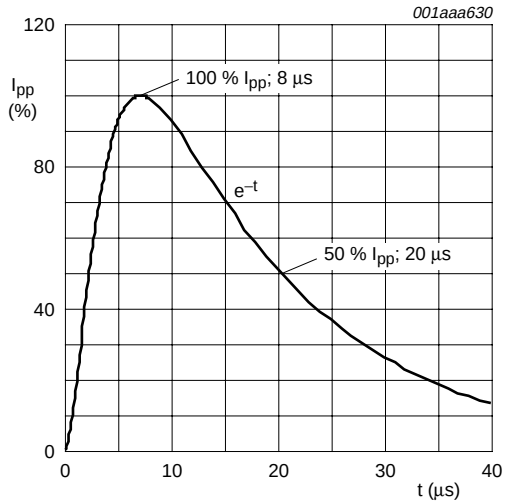

Fig 1. 8/20 µs pulse waveform according to IEC 61000-4-5.

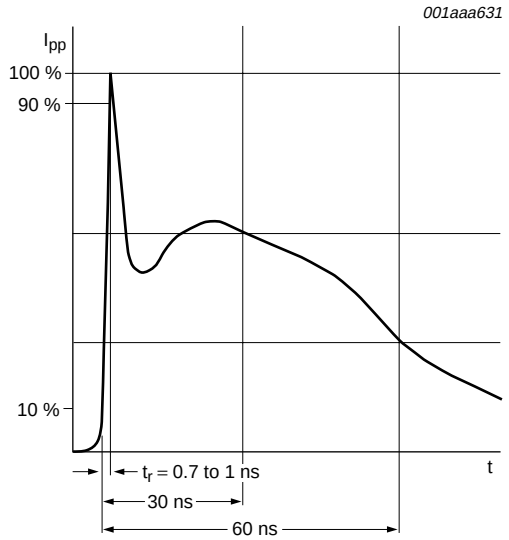

Fig 2. ElectroStatic Discharge (ESD) pulse waveform according to IEC 61000-4-2.

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6. Characteristics

Table 8: Characteristics

T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{RWM}	reverse stand-off voltage					
	PESD1LIN (15 V)		-	-	15	V
	PESD1LIN (24 V)		-	-	24	V
I_{RM}	reverse leakage current					
	PESD1LIN (15 V)	V_{RWM} = 15 V	-	< 1	50	nA
	PESD1LIN (24 V)	V_{RWM} = 24 V	-	< 1	50	nA
$V_{(BR)}$	breakdown voltage	I_R = 5 mA				
	PESD1LIN (15 V)		17.1	18.9	20.3	V
	PESD1LIN (24 V)		25.4	27.8	30.3	V
C_d	diode capacitance	V_R = 0 V; f = 1 MHz	-	13	17	pF
$V_{(CL)R}$	clamping voltage		[1]			
	PESD1LIN (15 V)	I_{PP} = 1 A	-	-	25	V
		I_{PP} = 5 A	-	-	44	V
	PESD1LIN (24 V)	I_{PP} = 1 A	-	-	40	V
		I_{PP} = 3 A	-	-	70	V
r_{dif}	differential resistance					
	PESD1LIN (15 V)	I_R = 1 mA	-	-	225	Ω
	PESD1LIN (24 V)	I_R = 1 mA	-	-	300	Ω

[1] Non-repetitive current pulse 8/20 μs exponentially decaying waveform; see Figure 1.

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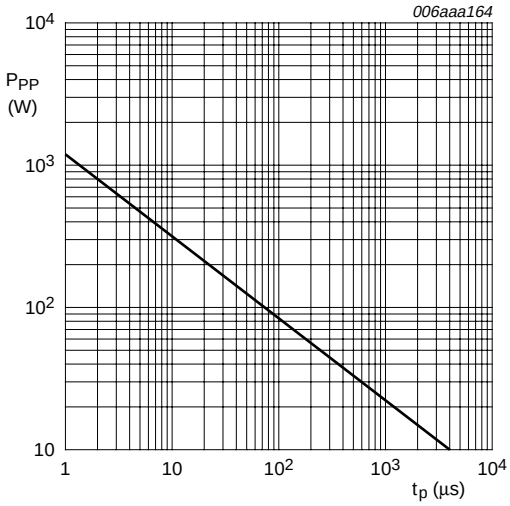

T_{amb} = 25 °C.

t_p = 8/20 µs exponentially decaying waveform; see Figure 1.

Fig 3. Peak pulse power dissipation as a function of pulse time; typical values.

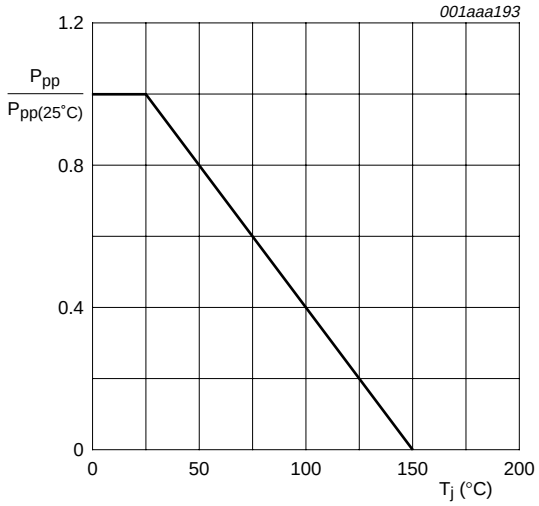

Fig 4. Relative variation of peak pulse power as a function of junction temperature; typical values.

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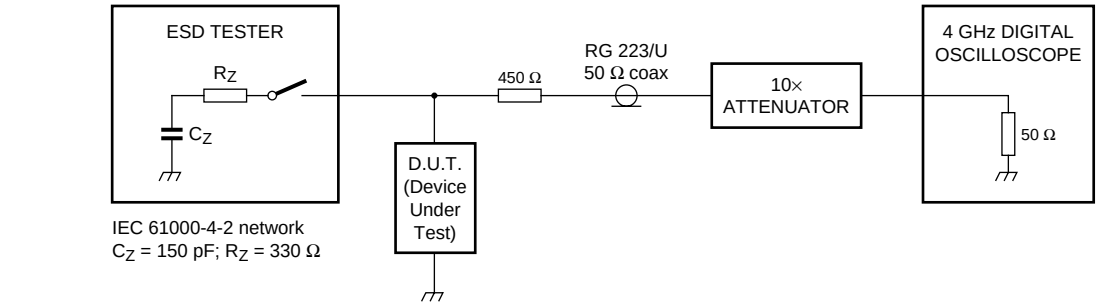
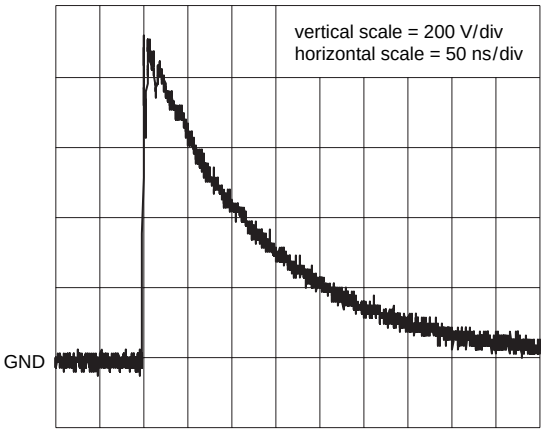
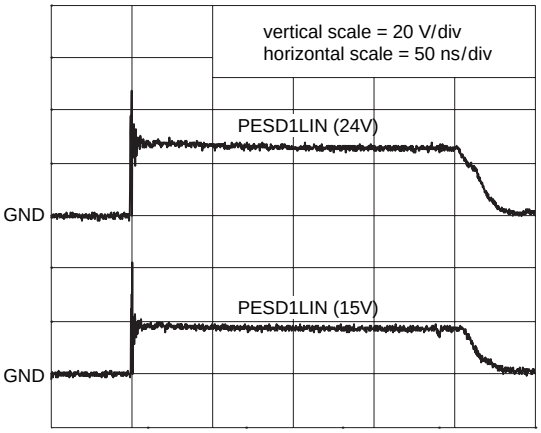
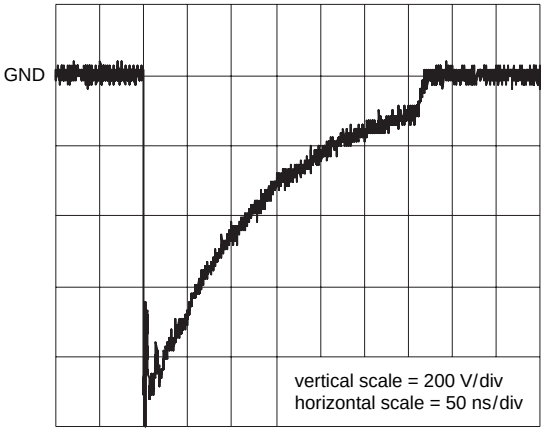




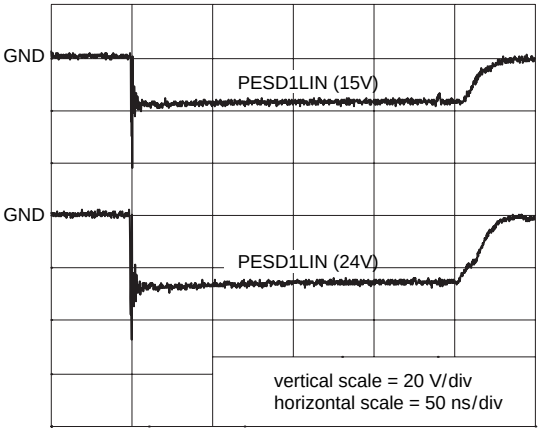
unclamped +1 kV ESD voltage waveform
(IEC 61000-4-2 network)



clamped +1 kV ESD voltage waveform
(IEC 61000-4-2 network)



unclamped −1 kV ESD voltage waveform
(IEC 61000-4-2 network)



clamped −1 kV ESD voltage waveform
(IEC 61000-4-2 network)

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Fig 5. ESD clamping test set-up and waveforms.

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7. Application information

The PESD1LIN is designed for protection of one LIN bus signal line from the damage caused by ElectroStatic Discharge (ESD) and surge pulses. The PESD1LIN provides a surge capability of up to 160 W per line for a 8/20 μs waveform.

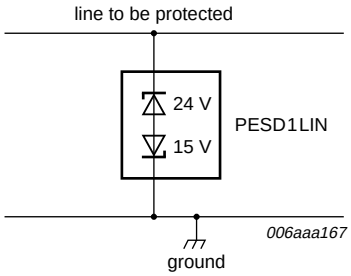

Fig 6. Typical application: ESD protection of one automotive LIN bus line.

Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

1. Place the protection device as close to the input terminal or connector as possible.
2. The path length between the protection device and the protected line should be minimized.
3. Keep parallel signal paths to a minimum.
4. Avoid running protection conductors in parallel with unprotected conductor.
5. Minimize all printed-circuit board conductive loops including power and ground loops.
6. Minimize the length of the transient return path to ground.
7. Avoid using shared transient return paths to a common ground point.
8. Ground planes should be used whenever possible. For multilayer printed-circuit boards, use ground vias.

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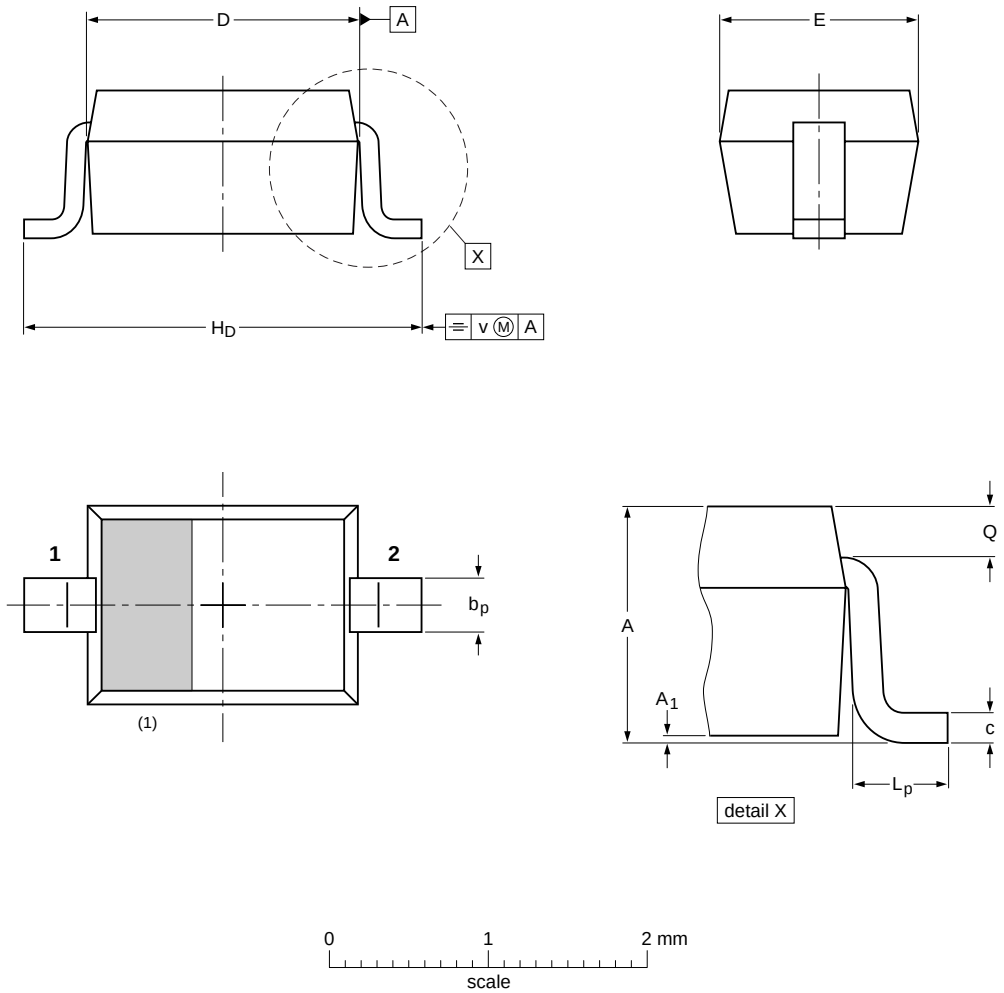
8. Package outline

Plastic surface mounted package; 2 leads **SOD323**



DIMENSIONS (mm are the original dimensions)

UNIT	A	A_1 max	b_p	c	D	E	H_D	L_p	Q	v
mm	1.1 0.8	0.05	0.40 0.25	0.25 0.10	1.8 1.6	1.35 1.15	2.7 2.3	0.45 0.15	0.25 0.15	0.2

Note

1. The marking bar indicates the cathode

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	JEITA			
SOD323			SC-76			~~99-09-13~~ 03-12-17

Fig 7. Package outline SOD323 (SC-76).

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9. Packing information

Table 9: Packing methods

The indicated -xxx are the last three digits of the 12NC ordering code. [1]

Type number	Package	Description	Packing quantity	
			3000	10000
PESD1LIN	SOD323	4 mm pitch, 8 mm tape and reel	-115	-135

[1] For further information and the availability of packing methods, see Section 14.

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10. Revision history

Table 10: Revision history

Document ID	Release date	Data sheet status	Change notice	Doc. number	Supersedes
PESD1LIN_1	20041026	Product data sheet	-	9397 750 14032	-

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11. Data sheet status

Level	Data sheet status [1]	Product status [2] [3]	Definition
I	Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
II	Preliminary data	Qualification	This data sheet contains data from the preliminary specification. Supplementary data will be published at a later date. Philips Semiconductors reserves the right to change the specification without notice, in order to improve the design and supply the best possible product.
III	Product data	Production	This data sheet contains data from the product specification. Philips Semiconductors reserves the right to make changes at any time in order to improve the design, manufacturing and supply. Relevant changes will be communicated via a Customer Product/Process Change Notification (CPCN).

[1] Please consult the most recently issued data sheet before initiating or completing a design.

[2] The product status of the device(s) described in this data sheet may have changed since this data sheet was published. The latest information is available on the Internet at URL http://www.semiconductors.philips.com.

[3] For data sheets describing multiple type numbers, the highest-level product status determines the data sheet status.

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Limiting values definition — Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 60134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

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Date of release: 26 October 2004
Document number: 9397 750 14032

Published in The Netherlands